

16750B-01

S E R V I C E N O T E

Supersedes:
NONE

Agilent 16750B-series Logic Analyzer Modules

Serial Numbers: 0000A00000/9999Z99999, MY00000000/MY999999999

Correction to the Agilent 16750B-series logic analyzer module field calibration procedure

Parts Required:

P/N	Description	Qty.
NONE		

Situation:

In the 400 MHz State Mode test on page 128 of the service manual, the operator is directed to assign a double edge to the logic analyzer J-clock. As it is configured, the pulse generator will not deliver the correct clock signal for double edge clocking. Consequently the operator will observe test data not consistent with the service manual calibration procedure.

Solution/Action:

Configure the pulse generator for divide-by-1 on channel 2. The table under Set up the equipment step 2 (page 119) shall read:

Timebase	Channel 2
Period: 5.000 ns	Mode: SQUARE Divide: SQUARE ÷ 1

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:

INFORMATION ONLY

AUTHOR: RB PRODUCT LINE: 1A

ADDITIONAL INFORMATION: 16750B-series Service Manual 16750-97002