

S E R V I C E N O T E

SUPERSEDES: NONE

83751A Synthesized Sweeper**Serial Numbers:** 0000A00000 / 9999Z99999**Firmware patch history****Duplicate Service Notes:**

83751A-04

83751B-03

83752A-05

83752B-04

Situation:

Several firmware patches have been added to the 8375x series. This service note documents the selftest patches for the 8375x Series and covers all patches up to July 26, 1996.

Solution/Action:

This information should be used to verify firmware patches when firmware is updated or the A4 CPU assembly is replaced and self-tests fails.

To view patches, press: Shift, Special, 101, Hz/s/ENTER. When "*** No Patches Existing**" is displayed, no patches exist.

Test patches maybe model and firmware rev. specific. Be sure to verify the firmware rev. and model numbers before entering a patch.

Continued

DATE: October 1996

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:

INFORMATION ONLY

AUTHOR:

ENTITY:

ADDITIONAL INFORMATION:

DM

5320



List of Firmware Patches:

A. Patches for all firmware rev. and A/B models:

1. Board Test = A9 ALC Test = ModDriverOff
Min. = 4.850 Max. = 5.200
2. Board Test = A9 ALC Test = HETModDrvOff
Min. = 4.850 Max. = 5.200
3. Board Test = A7 FracN Test = PretunDACmin
** Test Skipped **
4. Board Test = A5 Timer Test = REF_UNLOCK
** Test Skipped **

B. In addition to the first 4 patches, if firmware rev. is A01.00 and A/B models include patches 5 through 7:

5. Board Test = A12 RF Interface Test = ModAmp1
Min. = 0.100 Max.=0.180
6. Board Test = A12 RF Interface Test = ModAmp2
Min. = 0.10 Max. = 0.180
7. Board Test = A12 RF Interface Test = DYOSenseLo
Min. = 0.175 Max. = 0.350

C. In addition to the first 7 patches, if firmware rev. A.01.00 and a B model include patches 8 through 10:

8. Board Test = A9 ALC Test = HiBdDetIn_B
Min. = -0.080 Max. = -0.008
9. Board Test = A9 ALC Test = LowBdDetIn_B
Min. = -0.080 Max. = -0.008
10. Board Test = A9 ALC Test = HetBdDetIn_B
Min. = -0.080 Max. = -0.008 Board Test = A9 ALC

How to Enter Firmware Patches

The full procedure for editing patches is located in the 83785x Service Guide, section 8.

Skip Patches

To insert a "SKIP" selftest patch, press: Shift, Special, 100, Hz/s/ENTER. Next:

1. Select the selftest to skip and press: Hz/s/ENTER.
2. Press: Shift, . (period)

3. In the left-hand window the selftest to be skipped will be displayed along with “SKIPTTEST” or “NEWLIMITS”. Use the arrow keys to select “SKIPTTEST” and then press: Hz/s/ENTER.

Test Limit Patches

To enter new test limits, press: Shift, Special, 100, Hz/s/ENTER.

1. To add a test patch select the selftest and press: Hz/s/ENTER.
2. Next press: Shift, . (period).
3. In the left-hand window the selftest will be displayed along with “SKIPTTEST” or “NEWLIMITS”. Next use the arrow keys to select “NEWLIMITS”.
4. Use the right hand and left arrow keys to move the pointers above the value to be modified.
5. Use the front panel knob to change the values to the new limits.
6. To enter the new self test values, press: Hz/s/ENTER.
7. Pressing Hz/s/ENTER again after entering the selftest patch runs that selftest. After running selftest, to gain instrument control press: PRESET.