

MODIFICATION RECOMMENDED

**E8257D-26B**

# **S E R V I C E N O T E**

Supersedes:  
E8257D-26A

## E8257D Analog Signal Generator

**Serial Numbers:**

**MY00000000/MY49282068, SG00000000/SG49280250 and US00000000/US49280539  
MY50420000/MY50420048 and US50420000/US50420017**

### **Intermittent CPU boot-up and/or No display**

**Parts Required:**

<b>P/N</b>	<b>Description</b>	<b>Qty.</b>
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None

## ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:			
<b>MODIFICATION RECOMMENDED</b>			
ACTION CATEGORY:	X ON SPECIFIED FAILURE [ ] AGREEABLE TIME	STANDARDS	LABOR: 0.5 Hours
LOCATION CATEGORY:	X CUSTOMER INSTALLABLE [ ] ON-SITE X SERVICE CENTER [ ] CHANNEL PARTNER	SERVICE INVENTORY: [ ] RETURN [ ] SCRAP [ ] SEE TEXT	USED PARTS: [ ] RETURN [ ] SCRAP [ ] SEE TEXT
AVAILABILITY:	Always	NO CHARGE AVAILABLE UNTIL 2/2/13	
[ ] Calibration Required X Calibration NOT Required		PRODUCT LINE: 15 AUTHOR: PY	
ADDITIONAL INFORMATION:			

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October 15, 2012

Rev. 20



**Situation:**

The clock buffer, U7 on the CPU board (E8251-60006 or E8251-60661 Revision 004 and below), may cause intermittent boot up and/or no display when the instrument is powered up. It is recommended that this modification is performed on any instrument within the serial numbers above that exhibits an intermittent boot-up and/or a blank display.

**Solution/Action:**

Using the appropriate soldering technique and ESD station, perform a modification on the U7 circuit on the CPU board. Connect U7 pin 8 to 3.4V (wire pin 8 to pin 4) and connect U7 pin 9 to DCOM (wire pin 9 to pin 12).

**U7 modification procedure:**

1. Refer to E8257D Service Guide for the A18 CPU removal procedure.
2. Refer to Figure 1 for U7.
3. Use 30 AWG wire for wiring pin 8 to pin 4, and pin 9 to pin 12 per Figure 1.
4. Re-install CPU and cover.
5. Run self test.
6. No calibration is required.

Figure 1 U7 wire modification

